



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/635,715
Confirmation No.2007
Filing DateAugust 5, 2003
Inventor..... W.G. Scott
AssigneeMicron Technology, Inc.
Group Art Unit.....2812
ExaminerRichard A. Booth
Attorney's Docket No.MI22-2375
Customer No.021567
Title: Methods of Forming Transistor Gates; and Methods of Forming Programmable
Read-Only Memory Constructions

RESPONSE TO MARCH 22, 2005 OFFICE ACTION

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

AMENDMENTS